

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 2001-251886

(43)Date of publication of application : 14.09.2001

(51)Int.Cl.

H02P 6/08
H01L 27/04
H01L 21/822
H01L 29/78

(21)Application number : 2000-058021

(71)Applicant : HITACHI LTD

HITACHI HARAMACHI
SEMICONDUCTOR LTD

(22)Date of filing : 29.02.2000

(72)Inventor : OURA HITOSHI

TANAKA TAKASHI

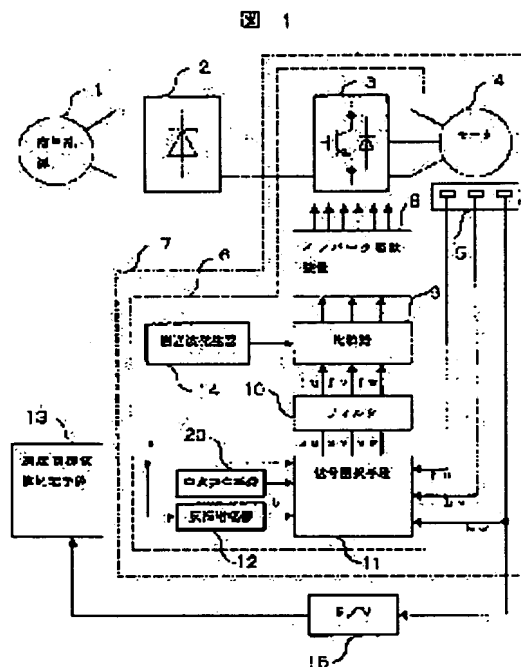
KAWABATA YUKIO

(54) CIRCUIT AND METHOD FOR DRIVE OF MOTOR AS WELL AS SEMICONDUCTOR INTEGRATED-CIRCUIT DEVICE

(57)Abstract:

PROBLEM TO BE SOLVED: To drive a motor at a low noise using a comparatively simple circuit.

SOLUTION: A plurality of command signals are generated according to the rotational speed of the motor. When any one of the plurality of command signals is selected on the basis of the magnetic-pole position of the motor, modulated waves are created. A power converter, which is used to supply drive electric power to the motor on the basis of the modulating waves, is controlled by means of pulse width modulation.



LEGAL STATUS

[Date of request for examination] 13.12.2001

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other

than the examiner's decision of rejection or
application converted registration]

[Date of final disposal for application]

[Patent number] 3442024

[Date of registration] 20.06.2003

[Number of appeal against examiner's
decision of rejection]

[Date of requesting appeal against
examiner's decision of rejection]

[Date of extinction of right]